ſ	L Number	Hits	Search Text	DB	Time stamp
Ī	1	8	"5784593"	USPAT;	2002/08/18 12:07
1				US-PGPUB;	2002,00,10 12.07
ı				IBM TDB	
1	_	461462	model\$3 or simulat\$4 or emulat\$4	USPAT;	2002/08/12 17:47
		101102	modely5 of Simulacy4 of Chalacy4	US-PGPUB;	2002/08/12 17:47
١					
١	_	189475	haundanu an Ifania aanal	IBM_TDB	0000/00/10 15 15
١.	-	1894/5	boundary or 'fanin cone'	USPAT;	2002/08/12 17:47
-				US-PGPUB;	
				IBM_TDB	
-	-	15	'fanin cone'	USPAT;	2002/08/12 17:57
-				US-PGPUB;	
- 1				IBM_TDB	
	-	25	"5469366"	USPAT;	2002/08/12 18:01
1				US-PGPUB;	
-				IBM TDB	İ
1	-	12952	(model\$3 or simulat\$4 or emulat\$4) and	USPAT;	2002/08/12 18:23
1			(boundary or 'fanin cone') and logic	US-PGPUB;	
1			, , , <u></u>	IBM TDB	
1	_	1034522	partit\$4 or division or cell	USPAT;	2002/08/12 18:24
1		1031322	partity of division of tell	US-PGPUB;	2002/06/12 18.24
	_	0010	//mada162 am aimulah64 am amulah64)	IBM_TDB	0000/00/10 10 04
	-	9918	((model\$3 or simulat\$4 or emulat\$4) and	USPAT;	2002/08/12 18:24
-			(boundary or 'fanin cone') and logic) and	US-PGPUB;	
			(partit\$4 or division or cell)	IBM_TDB	
	-	3229	(((model\$3 or simulat\$4 or emulat\$4) and	USPAT;	2002/08/12 18:25
1			(boundary or 'fanin cone') and logic) and	US-PGPUB;	
-	İ		(partit\$4 or division or cell)) and latch	IBM TDB	
-	- [1228	((((model\$3 or simulat\$4 or emulat\$4) and	USPAT;	2002/08/12 18:25
1			(boundary or 'fanin cone') and logic) and	US-PGPUB;	
			(partit\$4 or division or cell)) and latch)	IBM TDB	
			and node		
	_	376974	semiconductor or 'integrated circuit'	USPAT;	2002/08/12 18:26
1				US-PGPUB;	2002,00,12 10.20
1				IBM TDB	
İ	_	552944	semiconductor or 'integrated circuit' or	USPAT;	2002/08/12 18:27
	ļ	332344	processor	US-PGPUB;	2002/08/12 18.27
			processor		
	_	996	////modol62 on oimulat64 an amulat64)	IBM_TDB	0000/00/10 10 06
	_	990	(((((model\$3 or simulat\$4 or emulat\$4) and	USPAT;	2002/08/12 18:26
- 1			(boundary or 'fanin cone') and logic) and	US-PGPUB;	
- 1			(partit\$4 or division or cell)) and latch)	IBM_TDB	
-	1		and node) and (semiconductor or		
	1		'integrated circuit')		
	- (1188	(((((model\$3 or simulat\$4 or emulat\$4) and	USPAT;	2002/08/12 18:28
	İ		(boundary or 'fanin cone') and logic) and	US-PGPUB;	
	1		(partit\$4 or division or cell)) and latch)	IBM_TDB	
	į		and node) and (semiconductor or	-	
	['integrated circuit' or processor)		
	-	680	(((((model\$3 or simulat\$4 or emulat\$4)	USPAT;	2002/08/12 18:28
		1	and (boundary or 'fanin cone') and logic)	US-PGPUB;	
			and (partit\$4 or division or cell)) and	IBM TDB	
			latch) and node) and (semiconductor or		
			'integrated circuit' or processor)) and	ļ	
			map		
	_ 1	839	(((((model\$3 or simulat\$4 or emulat\$4)	ticnam.	2002/08/12 10:20
	ŀ	039		USPAT;	2002/08/12 18:30
ε	ľ		and (boundary or 'fanin cone') and logic)	US-PGPUB;	
	ľ		and (partit\$4 or division or cell)) and	IBM_TDB	
			latch) and node) and (semiconductor or		ļ
1			'integrated circuit' or processor)) and		
•			map\$4		
	-	419	(decompos\$4 or break\$3) and ((((((model\$3	USPAT;	2002/08/12 18:32
-	1		or simulat\$4 or emulat\$4) and (boundary or	US-PGPUB;	ļ
]		'fanin cone') and logic) and (partit\$4 or	IBM TDB	
	1		division or cell)) and latch) and node)	-	
			and (semiconductor or 'integrated circuit'		
1	1		or processor)) and map\$4)		
_			*		

	311	((decompos\$4 or break\$3) and	USPAT;	2002/08/12 18:3
		((((((model\$3 or simulat\$4 or emulat\$4)	US-PGPUB;	
		and (boundary or 'fanin cone') and logic)	IBM TDB	
		and (partit\$4 or division or cell)) and		
		latch) and node) and (semiconductor or		1
		'integrated circuit' or processor)) and		
		map\$4)) and verif\$7		
	205	l • · · ·	HCDAM.	2002/08/12 10:2
-	205		USPAT;	2002/08/12 18:3
		(((((((model\$3 or simulat\$4 or emulat\$4)	US-PGPUB;	
		and (boundary or 'fanin cone') and logic)	IBM_TDB	
		and (partit\$4 or division or cell)) and		
		latch) and node) and (semiconductor or		1
		'integrated circuit' or processor)) and		1
		map\$4)) and verif\$7) and (seed or cell)		1
-	21223	(decompos\$4 or break\$3 or segment\$3) same	USPAT;	2002/08/18 09:4
		(semiconductor or IC or 'Integrated	US-PGPUB;	
		circuit')	IBM TDB	
_	3424	((decompos\$4 or break\$3 or segment\$3) same	USPAT;	2002/08/18 09:20
	3727		US-PGPUB;	2002/00/10 03.2
		(semiconductor or IC or 'Integrated	1	1
	422	circuit')) and boundary	IBM_TDB	0000/00/10 00 0
-	437	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2002/08/18 09:2
		same (semiconductor or IC or 'Integrated	US-PGPUB;	
		circuit')) and boundary) and latch	IBM_TDB	1
-	221	((((decompos\$4 or break\$3 or segment\$3)	USPAT;	2002/08/18 09:4
	İ	same (semiconductor or IC or 'Integrated	US-PGPUB;	
		circuit')) and boundary) and latch) and	IBM TDB	
		(model\$3 or simulat\$4 or emulat\$4)	_	l .
_	2671	(segment\$5 and circuit).ab.	USPAT;	2002/08/18 09:4
	1 20,1	(Joginone for all a direction for the formation of the fo	US-PGPUB;	2002,00,10
			IBM TDB	
	274	//somestip and singuity ab) and boundary		2002/09/19 00:4
_	374	((segment\$5 and circuit).ab.) and boundary	USPAT;	2002/08/18 09:4
			US-PGPUB;	
	1		IBM_TDB	
-	75	, , , , , , , , , , , , , , , , , , ,	USPAT;	2002/08/18 09:4
		boundary) and latch	US-PGPUB;	
			IBM TDB	
-	881	(segment\$5 and (semiconductor or	USPAT;	2002/08/18 09:4
		'integrated circuit')).ab.	US-PGPUB;	
		,,,	IBM TDB	
_	98	((segment\$5 and (semiconductor or	USPĀT;	2002/08/18 09:4
		'integrated circuit')).ab.) and latch	US-PGPUB;	2002/00/10 03.4
		integrated circuit //.ab./ and laten	1	
	10	///	IBM_TDB	0000/00/10 00 4
-	19		USPAT;	2002/08/18 09:4
		'integrated circuit')).ab.) and latch) and	US-PGPUB;	
		boundary	IBM_TDB	
-	1890	((decompos\$4 or break\$3 or segment\$3) same	USPAT;	2002/08/18 09:4
		(semiconductor or IC or 'Integrated	US-PGPUB;	
		circuit')).ab.	IBM TDB	
_	145	(((decompos\$4 or break\$3 or segment\$3)	USPAT;	2002/08/18 09:4
		same (semiconductor or IC or 'Integrated	US-PGPUB;	2002,00,20
		circuit')).ab.) and latch	IBM TDB	
_	103	((((decompos\$4 or break\$3 or segment\$3)	USPAT;	2002/09/19 00:41
	103			2002/08/18 09:4
		same (semiconductor or IC or 'Integrated	US-PGPUB;	
		circuit')).ab.) and latch) and transistor	IBM_TDB	1
-	83	(((((decompos\$4 or break\$3 or segment\$3)	USPAT;	2002/08/18 09:5
		same (semiconductor or IC or 'Integrated	US-PGPUB;	1
		circuit')).ab.) and latch) and transistor)	IBM TDB	
		and (boundary or edge or cell)		
_	0	(decompos\$4 and boundary and latch).ab.	USPAT;	2002/08/18 09:5
		(accompost: and contacty and tacont.ab.	US-PGPUB;	2002/00/10 09.5
_	7.	(houndary and letak) ab	IBM_TDB	2002/00/10 10 0
_	74	(boundary and latch).ab.	USPAT;	2002/08/18 10:03
			US-PGPUB;	
			TDM TDD	

((boundary and latch).ab.) and decompos\$4

(decompos\$5 and latch).ab.

IBM TDB

USPAT; US-PGPUB; IBM TDB

USPAT; US-PGPUB; IBM_TDB 2002/08/18 09:55

2002/08/18 12:07

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